

<p>Substitute for form 1449A/PTO</p> <h1 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1> <p><i>(use as many sheets as necessary)</i></p>				C mplete if Known	
Sheet	1	of	1	Application Number	
				Filing Date	
				First Named Inventor	Gary L. Graunke
				Art Unit	
				Examiner Name	
				Attorney Docket Number	42P16433

OTHER ART - NON PATENT LITERATURE DOCUMENTS

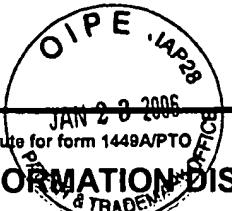
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/TBT/		Dworkin, Morris "Recommendation for Block Cipher Modes of Operation," <i>Methods and Techniques</i> , COMPUTER SECURITY, NIST, 2001 Edition, pp. i-vii, 9-16	

Examiner Signature	/Thanhnga Truong/	Date Considered	03/29/2007
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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Based on PTO/SB/08B (05-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 05/02/2003.
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**INFORMATION & TRADEMARK
DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1

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2

Complete If Known	
Application Number	10/603,680
Filing Date	June 25, 2003
First Named Inventor	Gary L. Graunke
Art Unit	2131
Examiner Name	TBD
Attorney Docket Number	42P16433

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS

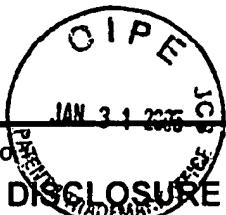
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/TBT/		Yang, J. et al., "Fast Secure Processor for Inhibiting Software Piracy and Tampering", Proc. of 36th Int'l Symp. on Microarchitecture, IEEE, pp. 351-360, 2003, XP010674237	

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